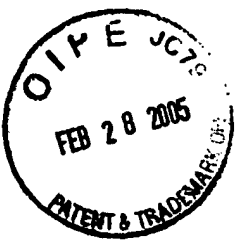


Serial No.: 10/003,602

Dkt. No. MST-012

TMJ 2/33
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventors: Wingyu Leung and Fu-Chieh Hsu

Assignee: Monolithic System Technology, Inc.

Title: Error Correcting Memory and Method of Operating
Same

Serial No: 10/003,602

Filing Date: 11/14/2001

Examiner: Christine Tu

Art Unit: 2133

Attorney Docket No: MST-012

Date: February 23, 2005

Mail Stop Amendment
COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT IN RESPONSE TO FIRST OFFICE ACTION

Sir:

Applicants submit the following remarks and amendments in response to the Office Action dated August 24, 2004. A petition for a three-month extension, extending the period for response to February 24, 2005, accompanies the present response.